

Notice of References Cited

Application/Control No.

09/805,138

Applicant(s)/Patent Under
Reexamination
MOORE ET AL.

Examiner

Yolanda Wilson

Art Unit

2184

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U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|---------------------|----------------|
| | A | US-5,790,874 | 08-1998 | Takano et al. | 713/320 |
| | B | US-5,276,824 A | 01-1994 | Skruhak et al. | 712/212 |
| | C | US-2002/0013912 | 01-2002 | Lin, Chong Ming | 713/320 |
| | D | US-5,732,255 | 03-1998 | Verbauwhede, Ingrid | 712/248 |
| | E | US-5,862,148 A | 01-1999 | Typaldos et al. | 714/724 |
| | F | US-6,237,101 B1 | 05-2001 | Moore et al. | 713/320 |
| | G | US-5,867,644 A | 02-1999 | Ranson et al. | 714/39 |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Brennan et al. Low Power Methodology and Design Techniques for Processor Design. ACM. August 10-12, 1998. pages 268-273. |
| | V | |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.